

ABSTRACT

An interface circuit for detecting, filtering and conditioning alternating current (AC) voltage input signals from external controllers to be received by a microcontroller. Coupled between the outputs of the external controllers and the inputs of the microcontroller of the interface circuit are RS232 receivers, which are used to detect and condition AC voltage input signals. A voltage divider having specific value resistors may be coupled between the outputs of the external controllers and the inputs of the RS232 receivers to reduce the operational voltage to a narrow detection range, the combination allowing the microcontroller to interpret the status of the AC voltage input signal.

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